

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Rapport, et al.  
Patent No. : 7,542,304  
Issue Date : June 2, 2009  
Serial No. : 10/804,452  
Filed : March 19, 2004

Art Unit : 2841  
Examiner : Andargie M Aychillhum  
Conf. No. : 7279

Title : MEMORY EXPANSION AND INTEGRATED CIRCUIT STACKING SYSTEM  
AND METHOD

Attn.: Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL OF REQUEST FOR CERTIFICATE OF CORRECTION

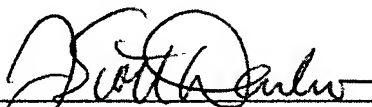
Applicants hereby request that a certificate of correction be issued for the above patent in accordance with the attached request.

All errors sought to be corrected were made in printing by the Patent and Trademark Office, and no fee is believed to be due.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 1, 2009

  
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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,542,304  
APPLICATION NO : 10/804,452  
DATED : JUNE 2, 2009  
INVENTOR(S) : RUSSELL RAPPORT

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 6, column 14, line 52, delete “y” and insert - - Y - -, therefor.

In Claim 8, column 14, line 64, delete “arc” and insert - - are - -, therefor.

In Claim 10, column 15, line 2, delete “arc” and insert - - are - -, therefor.

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